**[Design for Test Engineer - Central DFT](https://www.linkedin.com/jobs/view/2544924047/?alternateChannel=search&refId=%2FxHlQwPzag%2F1QDACuEvm6Q%3D%3D&trackingId=e0mORApIaZyc0xvGYYXQQQ%3D%3D&trk=d_flagship3_search_srp_jobs)**

[AMD](https://www.linkedin.com/company/amd/life/)  Markham, ON On-site 2 weeks ago  54 applicants

* Full-time · Mid-Senior level
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Meet the hiring team

[[](https://www.linkedin.com/in/don-sweet-8a4b3a)**Don Sweet**](https://www.linkedin.com/in/don-sweet-8a4b3a)- 3rd

Senior Talent Acquisition Advisor at AMD

Job poster

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**What You Do At AMD Changes Everything**At AMD, we push the boundaries of what is possible. We believe in changing the world for the better by driving innovation in high-performance computing, graphics, and visualization technologies – building blocks for gaming, immersive platforms, and the data center.  
  
Developing great technology takes more than talent: it takes amazing people who understand collaboration, respect, and who will go the “extra mile” to achieve unthinkable results. It takes people who have the passion and desire to disrupt the status quo, push boundaries, deliver innovation, and change the world. If you have this type of passion, we invite you to take a look at the opportunities available to come join our team.  
  
**Role**Design for Test Engineer - Central DFT The Role  
  
Central DFX (CDFX) is a centralized ASIC design group within AMD’s Technology and Engineering organization. The group consists of design teams located in several AMD locations in North America and Asia. It is primarily responsible for architecture, design and implementation of critical Design-for-Test (DFT) and Design-for-Debug (DFD) features for leading edge AMD products. It is also responsible for DFX design methodology and automation tools development to support the global DFX engineering teams across AMD.  
  
**The Person**As a Silicon Design Engineer, you will be working with a diverse team of design engineers and managers from various global design locations on design-for-test (DFT) design and implementation, tool and methodology development, project execution and continuous improvement initiatives. This role provides an excellent growth opportunity for robust individuals looking to make a difference. This is an exciting time to join the AMD team!  
  
**The Following Highlights a Successful Candidate**

* Demonstrated DFT design skills and project execution experience
* Excellent verbal and written communication and interpersonal skills
* Self-starter, driven and disciplined with a dedication to meeting deadlines
* Has an aptitude to thrive in a fast-paced multi-tasking environment
* Used to working independently, and yet can work collaboratively with various levels and organization functions.

**Key Responsibilities:**

* Working with a multi-discipline and international team of engineers on design-for-test (DFT) architecture, design, tools and methodology initiatives
* Developing CAD software, scripts and other support technology to enable successful construction of DFT logics in complex SoC design
* Performing scan insertion, ATPG verification and test pattern generation
* Writing and maintain DFT documentation and specifications
* Performing DFT RTL integration, synthesis, equivalency checking, timing analysis and closure including defining design constraints
* Performing DFT RTL design per micro-architectural specifications using design generation flows
* Providing DFT feature bring-up and pattern debug support to production engineering team during first silicon bring-up, qualification and failure analysis
* This job also involves using the test automation software for data collection one week per year at one of the USA national laboratories

**Preferred Experience:**

* Demonstrated ASIC design experience
* Demonstrated technical leadership and works well with cross-functional teams
* Excellent communication and interpersonal skills
* Expertise in scan compression architecture, scan insertion and ATPG methodologies are essential
* Experience in complex ASIC design (multi-million gates) in DFT/DFD techniques such as JTAG/IEEE standards, scan and ATPG, on-chip test pattern compression and at-speed testing using PLL, memory BIST and repair, logic BIST, power-gating, on-chip debug logic, testing of high speed SerDes IO and analog design
* Understanding various technologies that must work with DFT/DFD technology such as CPU’s, graphics engines, high-speed digital design, memory and I/O controllers, … etc
* Working knowledge and experience in verilog simulator and waveform debugging tools, proficiency in debugging both RTL and gate level simulations
* Experience in solving logic design or timing issues with integration, synthesis and PD teams
* Good working knowledge of UNIX/Linux and scripting languages (e.g., TCL, c-shell, Perl), C++ programming
* Knowledge in EDA tools/methodology, such as synthesis, equivalency checking, static timing analysis
* Knowledge of ATE and digital IC manufacturing test is a plus
* Knowledge in using emulator for ATPG pattern verification is a plus

**Academic Credentials:**

* Minimum B.Sc in Electrical or Computer Engineering (or equivalent)

**Location:**Markham, Ontario, Canada  
  
AMD is an inclusive employer dedicated to building a diverse workforce. We encourage applications from all qualified candidates and will accommodate applicants’ needs under the respective provincial human rights codes throughout all stages of the recruitment and selection process. Any applicant who requires accommodation should contact AskHR@amd.com.  
  
AMD does not accept unsolicited resumes from headhunters, recruitment agencies or fee based recruitment services.  
  
**Requisition Number:**96162  
  
**Country:**Canada **Province:**Ontario **City:**Markham  
  
**Job Function:Design**AMD is an inclusive employer dedicated to building a diverse workforce. We encourage applications from all qualified candidates and will accommodate applicants’ needs under the respective provincial human rights codes throughout all stages of the recruitment and selection process. Any applicant who requires accommodation should contact AskHR@amd.com .  
  
AMD does not accept unsolicited resumes from headhunters, recruitment agencies or fee based recruitment services.

**Pay range unavailable**

Salary information is not available at the moment.

Are you interested in salary information for this job?Yes/No

**About the company**

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For nearly 50 years, AMD (NASDAQ: AMD) has driven innovation in high-performance computing, graphics, and visualization technologies ― the building blocks for gaming, immersive platforms, and the datacenter. Hundreds of millions of consumers, leading Fortune 500 businesses, and cutting-edge scientific research facilities around the world rely on AMD technology daily to improve how they live, work, and play.  
  
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